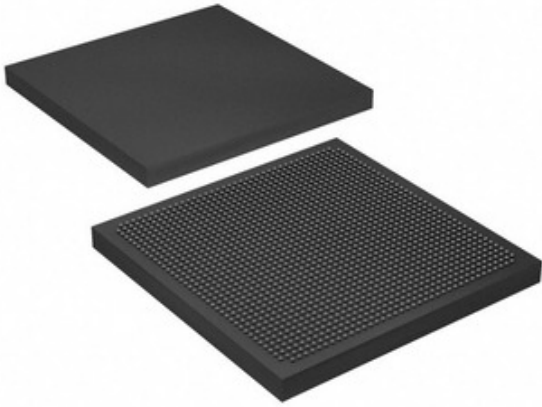




Intel® FPGAs



10AS066N3F40E2LG

Part Number:

Intel® FPGAs

IC SOC FPGA 588 I/O 1517FBGA

1.10AS066N3F40E2LG.pdf

2.10AS066N3F40E2LG.pdf

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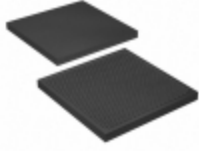
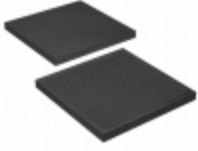
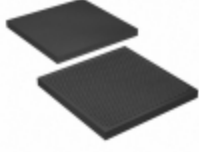
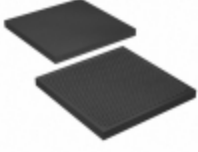
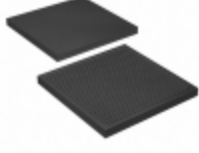
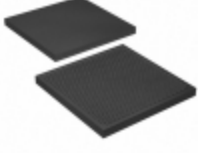
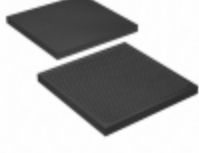
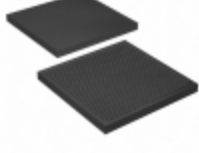
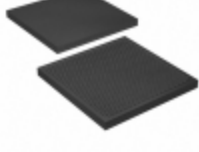
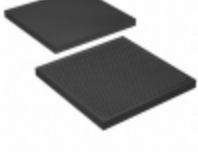
Specifications of 10AS066N3F40E2LG

PART NUMBER	10AS066N3F40E2LG
MANUFACTURER	Intel® FPGAs
DESCRIPTION	IC SOC FPGA 588 I/O 1517FBGA
LEAD FREE STATUS / ROHS STATUS	Lead free / RoHS Compliant
DATA SHEET	1.10AS066N3F40E2LG.pdf 2.10AS066N3F40E2LG.pdf 3.10AS066N3F40E2LG.pdf
SUPPLIER DEVICE PACKAGE	1517-FCBGA (40x40)
SPEED	1.5GHz
SERIES	Arria 10 SX
RAM SIZE	256KB
PRIMARY ATTRIBUTES	FPGA - 660K Logic Elements
PERIPHERALS	DMA, POR, WDT
PACKAGING	Tray
PACKAGE / CASE	1517-BBGA, FCBGA
OPERATING TEMPERATURE	0°C ~ 100°C (TJ)
NUMBER OF I/O	588
MOISTURE SENSITIVITY LEVEL (MSL)	3 (168 Hours)
MANUFACTURER STANDARD LEAD TIME	12 Weeks
LEAD FREE STATUS / ROHS STATUS	Lead free / RoHS Compliant
FLASH SIZE	-
DETAILED DESCRIPTION	Dual ARM® Cortex®-A9 MPCore™ with CoreSight™ System On Chip (SOC) IC Arria 10 SX FPGA - 660K Logic Elements 256KB 1.5GHz 1517-FCBGA (40x40)
CORE PROCESSOR	Dual ARM® Cortex®-A9 MPCore™ with CoreSight™
CONNECTIVITY	EBI/EMI, Ethernet, I²C, MMC/SD/SDIO, SPI, UART/USART, USB OTG
ARCHITECTURE	MCU, FPGA

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