



Xilinx



ALL PROGRAMMABLE.

XCZU5EG-L1FBVB900I

Part Number:	XCZU5EG-L1FBVB900I
Manufacturer/Brand:	Xilinx
Product description	IC FPGA 204 I/O 900FCBGA  1.XCZU5EG-L1FBVB900I.pdf  2.XCZU5EG-L1FBVB900I.pdf  3.XCZU5EG-L1FBVB900I.pdf  4.XCZU5EG-L1FBVB900I.pdf
Datasheets:	
RoHs Status	 Lead free / RoHS Compliant
Ship From	Hong Kong
Shipment Way	DHL/Fedex/TNT/UPS/EMS

REQUEST FOR QUOTATION

Image may be representation. See specifications for product details.

Specifications of XCZU5EG-L1FBVB900I

PART NUMBER	XCZU5EG-L1FBVB900I
MANUFACTURER	Xilinx
DESCRIPTION	IC FPGA 204 I/O 900FCBGA
LEAD FREE STATUS / ROHS STATUS	Lead free / RoHS Compliant
DATA SHEET	 1.XCZU5EG-L1FBVB900I.pdf  2.XCZU5EG-L1FBVB900I.pdf  3.XCZU5EG-L1FBVB900I.pdf  4.XCZU5EG-L1FBVB900I.pdf
SUPPLIER DEVICE PACKAGE	900-FCBGA (31x31)
SPEED	500MHz, 600MHz, 1.2GHz
SERIES	Zynq® UltraScale+™ MPSoC EG
RAM SIZE	256KB
PRIMARY ATTRIBUTES	Zynq®UltraScale+™ FPGA, 256K+ Logic Cells
PERIPHERALS	DMA, WDT
PACKAGING	Tray
PACKAGE / CASE	900-BBGA, FCBGA
OPERATING TEMPERATURE	-40°C ~ 100°C (TJ)
NUMBER OF I/O	204
MOISTURE SENSITIVITY LEVEL (MSL)	4 (72 Hours)
MANUFACTURER STANDARD LEAD TIME	10 Weeks
LEAD FREE STATUS / ROHS STATUS	Lead free / RoHS Compliant
FLASH SIZE	-
DETAILED DESCRIPTION	Quad ARM® Cortex®-A53 MPCore™ with CoreSight™, Dual ARM®Cortex™-R5 with CoreSight™, ARM Mali™-400 MP2 System On Chip (SOC) IC Zynq® UltraScale+™ MPSoC EG Zynq®UltraScale+™ FPGA, 256K+ Logic Cells 256KB 500MHz, 600MHz, 1.2GHz 900-FCBGA (31x31)
CORE PROCESSOR	Quad ARM® Cortex®-A53 MPCore™ with CoreSight™, Dual ARM®Cortex™-R5 with CoreSight™, ARM Mali™-400 MP2
CONNECTIVITY	CANbus, EBI/EMI, Ethernet, I²C, MMC/SD/SDIO, SPI, UART/USART, USB OTG
ARCHITECTURE	MCU, FPGA

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