1. General description

PNP high power bipolar transistor in a SOT669 (LFPAK56) Surface-Mounted Device (SMD) power plastic package.

NPN complement: PHPT61002NYCLH.

2. Features and benefits

- High thermal power dissipation capability
- Suitable for high temperature applications up to 175 °C
- · Reduced Printed-Circuit Board (PCB) requirements comparing to transistors in DPAK
- High energy efficiency due to less heat generation

3. Applications

- Power management
- · Load switch
- Linear mode voltage regulator
- Backlighting applications

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CEO}	collector-emitter voltage	open base		-	-	-100	V
I _C	collector current			-	-	-2	Α
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-	-5	Α
R _{CEsat}	collector-emitter saturation resistance	$I_C = -2 \text{ A}; I_B = -200 \text{ mA}; T_{amb} = 25 \text{ °C}$	[1]	-	150	250	mΩ

[1] Pulse test: $t_0 \le 300 \,\mu\text{s}$; $\delta \le 0.02$



5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	Е	emitter	mb	C -
2	Е	emitter		В
3	Е	emitter	d	<u>~</u>
4	В	base		E sym132
mb	С	collector	1 2 3 4 LFPAK56; Power- SO8 (SOT669)	

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PHPT61002PYCLH	LFPAK56; Power-SO8	Plastic single-ended surface-mounted package (LFPAK56; Power-SO8); 4 leads	SOT669		

7. Marking

Table 4. Marking codes

Type number	Marking code
PHPT61002PYCLH	1002PCC

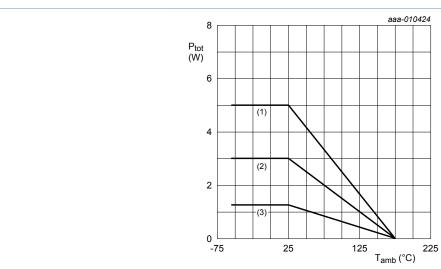
8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-100	V
V_{CEO}	collector-emitter voltage	open base		-	-100	V
V _{EBO}	emitter-base voltage	open collector		-	-8	V
I _C	collector current			-	-2	Α
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-5	Α
I _B	base current			-	-0.5	Α
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	1.25	W
			[2]	-	3	W
			[3]	-	5	W
			[4]	-	25	W
Tj	junction temperature			-	175	°C
T _{amb}	ambient temperature			-55	175	°C
T _{stg}	storage temperature			-65	175	°C

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated mounting pad for collector 6 cm².
- [3] Device mounted on a ceramic PCB, Al₂O₃, standard footprint.
- [4] Power dissipation from junction to mounting base.



- (1) Ceramic PCB, Al₂O₃, standard footprint
- (2) FR4 PCB, mounting pad for collector 6 cm²
- (3) FR4 PCB, standard footprint

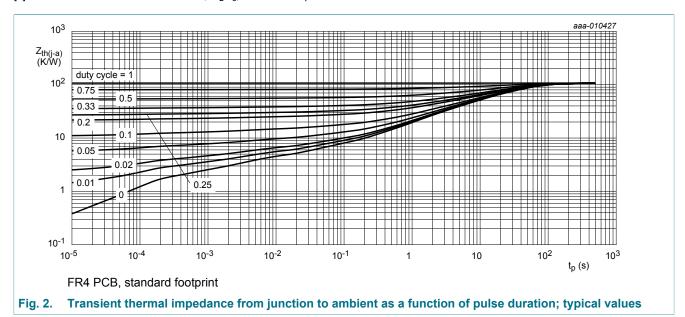
Fig. 1. Power derating curves

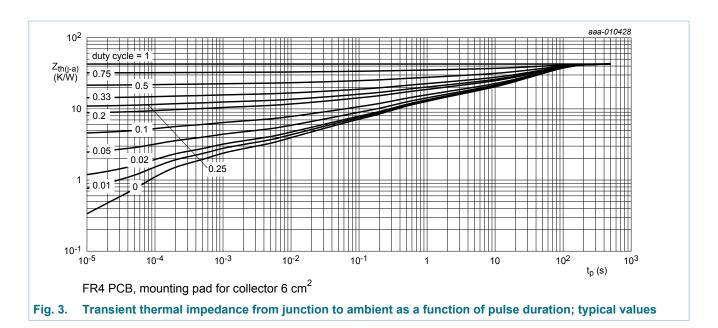
9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
ui(j'a)	thermal resistance from junction to ambient		[1]	-	-	115	K/W
			<u>[2]</u>	-	-	50	K/W
			<u>[3]</u>	-	-	30	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	-	6	K/W

- Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated mounting pad for collector 6 cm². [2] [3]
- Device mounted on a ceramic PCB, Al₂O₃, standard footprint.





10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off	V _{CB} = -80 V; I _E = 0 A; T _{amb} = 25 °C		-	-	-100	nA
	current	V _{CB} = -80 V; I _E = 0 A; T _j = 150 °C		-	-	-50	μΑ
I _{CES}	collector-emitter cut-off current	$V_{CE} = -80 \text{ V}; V_{BE} = 0 \text{ V}; T_{amb} = 25 \text{ °C}$		-	-	-100	nA
I _{EBO}	emitter-base cut-off current	$V_{EB} = -8 \text{ V}; I_{C} = 0 \text{ A}; T_{amb} = 25 ^{\circ}\text{C}$		-	-	-100	nA
h _{FE}	DC current gain	V_{CE} = -1.5 V; I_{C} = -500 mA; T_{amb} = 25 °C	[1]	80	160	-	
		V_{CE} = -10 V; I_{C} = -500 mA; T_{amb} = 25 °C	[1]	100	180	-	
		V_{CE} = -5 V; I_{C} = -1 A; T_{amb} = 25 °C	[1]	70	150	260	
		V_{CE} = -10 V; I_{C} = -1 A; T_{amb} = 25 °C	[1]	90	160	-	
		V _{CE} = -10 V; I _C = -2 A; T _{amb} = 25 °C	[1]	20	70	-	
V _{CEsat}	collector-emitter saturation voltage	$I_C = -0.5 \text{ A}; I_B = -50 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$		-	-75	-130	mV
		I_C = -2 A; I_B = -200 mA; T_{amb} = 25 °C	[1]	-	-300	-500	mV
R _{CEsat}	collector-emitter saturation resistance			-	150	250	mΩ
V_{BEsat}	base-emitter saturation voltage		[1]	-	-1.02	-1.2	V
V_{BEon}	base-emitter turn-on voltage	V_{CE} = -2 V; I_{C} = -0.1 A; T_{amb} = 25 °C	[1]	-	-0.67	-0.9	V
t _d	delay time	V_{CC} = -12.5 V; I_{C} = -1 A; I_{Bon} = -50 mA;		-	20	-	ns
t _r	rise time	I _{Boff} = 50 mA; T _{amb} = 25 °C		-	190	-	ns
t _{on}	turn-on time			-	210	-	ns
t _s	storage time			-	300	-	ns
t _f	fall time			-	170	-	ns
t _{off}	turn-off time			-	470	-	ns
f _T	transition frequency	V _{CE} = -10 V; I _C = -100 mA; f = 100 MHz; T _{amb} = 25 °C		-	125	-	MHz
C _c	collector capacitance	V _{CB} = -10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz; T _{amb} = 25 °C		-	28	-	pF

^[1] Pulse test: $t_p \le 300 \ \mu s; \ \delta \le 0.02$

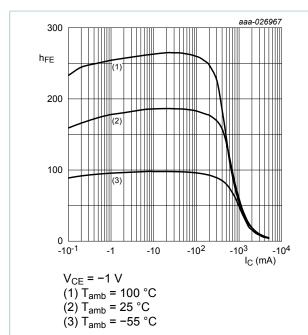


Fig. 4. DC current gain as a function of collector current; typical values

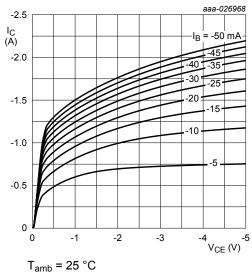


Fig. 5. Collector current as a function of collectoremitter voltage; typical values

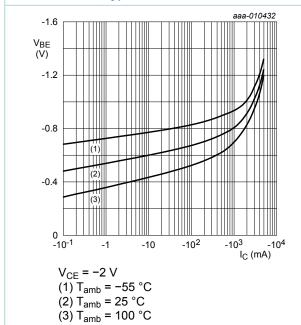
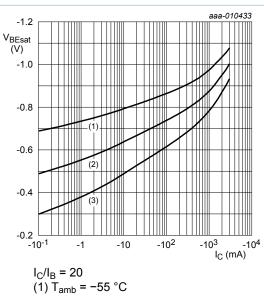


Fig. 6. Base-emitter voltage as a function of collector current; typical values



 $T_{C}/T_{B} = 20$ (1) $T_{amb} = -55 \,^{\circ}C$ (2) $T_{amb} = 25 \,^{\circ}C$ (3) $T_{amb} = 100 \,^{\circ}C$

Fig. 7. Base-emitter saturation voltage as a function of collector current; typical values

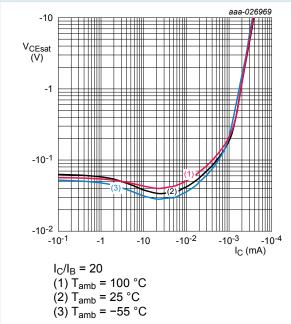


Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

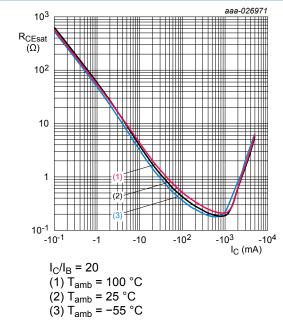


Fig. 10. Collector-emitter saturation resistance as a function of collector current; typical values

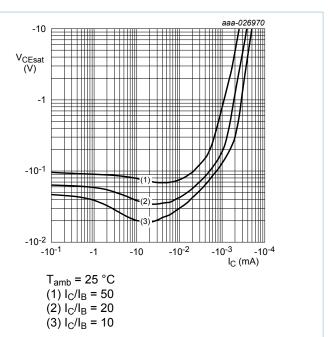


Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values

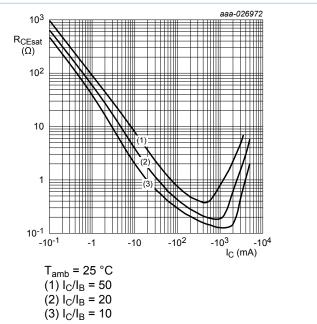
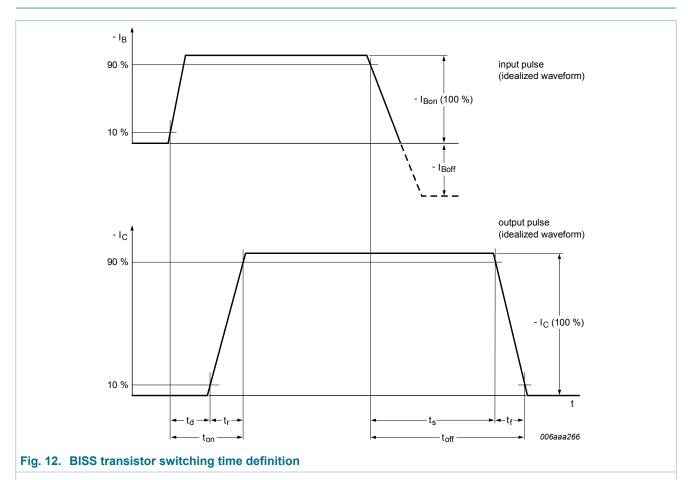


Fig. 11. Collector-emitter saturation resistance as a function of collector current; typical values

11. Test information



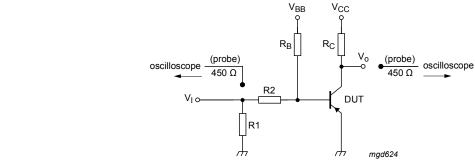
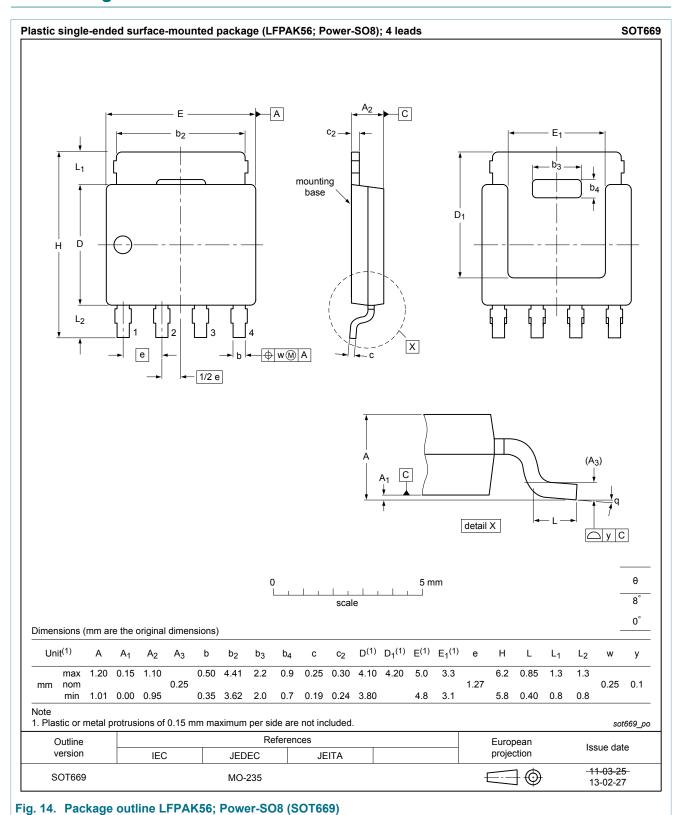
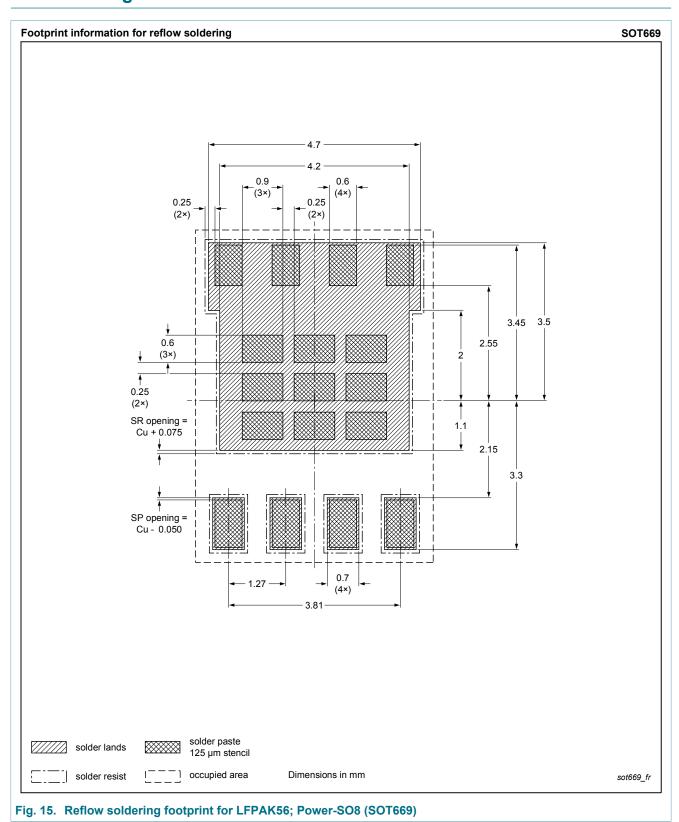


Fig. 13. Test circuit for switching times

12. Package outline



13. Soldering



14. Revision history

Table 8. Revision history

Table of Novicion motory							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
PHPT61002PYCLH v.1	20170713	Product data sheet	-	-			

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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