SSOP8-P-0,50A

Weight: 0.01 g (typ.)

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WB126FK

Dual Bus Switch

The TC7WB126FK is a low on-resistance, high-speed CMOS dual-bit bus switch. This bus switch allows the connections or disconnections to be made with minimal propagation delay while maintaining Low power dissipation which is the feature of CMOS.

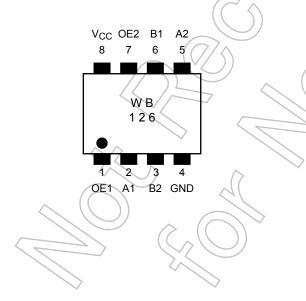
When output enable (OE) is at High level, the switch is on; when at Low level, the switch is off.

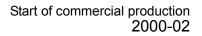
All inputs are equipped with protector circuits to protect the device from static discharge.

Features

- Operating voltage: VCC = 4.5 to 5.5 V
- High speed operation: t_{pd} = 0.25 ns (max)
- Ultra-low on resistance: $R_{ON} = 5 \Omega$ (typ.)
- ESD performance: Machine model $\ge \pm 200$ V Human body model $\ge \pm 2000$ V
- TTL level input (control input)
- Package: US8

Pin Assignment (top view)





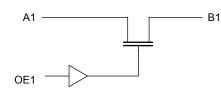
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B2

Truth Table

Inputs	Function					
OE	FUNCTION					
L	Disconnect					
Н	A port = B port					

System Diagram



Absolute Maximum Ratings (Note)

			$\left(\Omega \right) \wedge$
Characteristics	Symbol	Rating	Unit
Power supply range	V _{CC}	-0.5 to 7.0	y
DC input voltage	V _{IN}	-0.5 to 7.0	> v
DC switch voltage	tch voltage $V_{\rm S}$ -0.5 to 7.0		V
Input diode current	liк	-50	mA
Continuous channel current	I _S <	128	mA
Power dissipation	PD	200	mW
DC V _{CC} /GND current	ICC/IGND	±100	mA
Storage temperature	Tstg	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

A2

OE2

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions")" Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	Vec	4.5 to 5.5	V
Input voltage	VIN	0 to 5.5	V
Switch voltage	VS	0 to 5.5	V
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 10	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Electrical Characteristics

DC Characteristics (Ta = -40 to 85°C)

Character	istics	Symbol	Test Condition		Min	Typ. (Note 1)	Max	Unit	
Input voltage	"H" level	VIH			4.5 to 5.5	2.0	—	_	V
Input voltage	"L" level	V _{IL}	_		4.5 to 5.5	f		0.8	v
Input leakage cur	rent	I _{IN}	V _{IN} = 0 to 5.5 V		4.5 to 5.5	£	DF	±1.0	μA
Power off leakage	e current	I _{OFF}	A, B, OE = 0 to 5.5 V		0	770	_	±1.0	μA
Off-state leakage (switch off)	current	I _{SZ}	A, B = 0 to 5.5 V, OE = GND		4.5 to 5.5	\mathcal{O}	_	±1.0	μΑ
ON resistance (Note 2)		R _{ON}	$V_{IS} = 0 V$	$I_{IS} = 30 \text{ mA}$ $I_{IS} = 64 \text{ mA}$	4.5 4.5 4.5)/	5 5	7 7 15	Ω
		$V_{IS} = 2.4 \text{ V}, I_{IS} = 15 \text{ mA}$ 4.5		4.5			01 OI		
Quiescent supply current		ICC	$V_{IN} = V_{CC} \text{ or GND}$ $I_{OUT} = 0$		5.5	$\Diamond^{-}($	\bigcirc	10	μΑ
		ΔI_{CC}	$V_{IN} = 3.4 V$ (one input)		5.5			2.5	mA

Note 1: Typical values are at $V_{CC} = 5$ V and Ta = $25^{\circ}C_{2}$

AC Characteristics (Ta = -40 to 85° C)

Characteristics	Symbol	Test Condition	\square	V _{CC} (V)	Min	Max	Unit
Propagation delay time (bus to bus)	tрЦН tpHL	Figure 1, Figure 2	(Note)	4.5	_	0.25	ns
Output enable time	t _{pZL} t _{pZH}	Figure 1, Figure 3		4.5		4.0	ns
Output disable time	t _{pLZ} t _{pHZ}	Figure 1, Figure 3		4.5	_	5.5	ns

Note: The propagation delay time is calculated by the RC (on-resistance and load capacitance) time constant.

Capacitive Characteristics (Ta = 25°C)

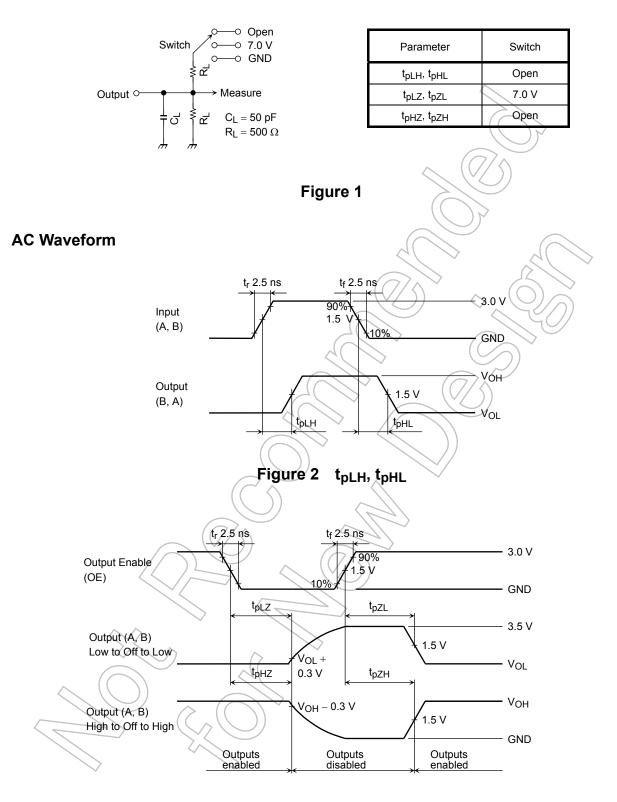
Characteristics	Symbol	Test Cond	lition	V _{CC} (V)	Тур.	Unit
Control pin input capacitance	CIN		(Note)	5.0	3	pF
Switch terminal capacitance	CI/O	OE = GND	(Note)	5.0	10	pF

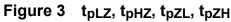
Note: This item is guaranteed by design.

Note 2: Measured by the voltage drop between A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the two (A or B) pins.

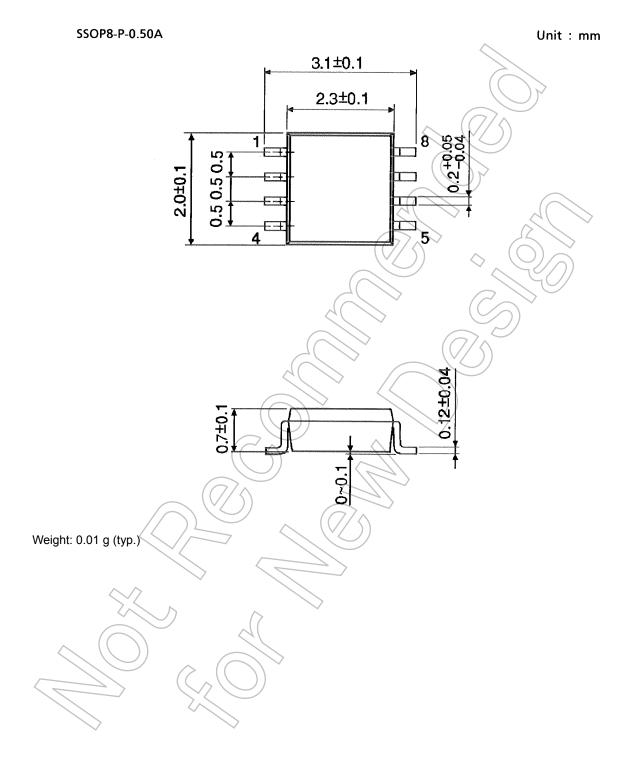
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AC Test Circuit





Package Dimensions



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