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**ANALOG
DEVICES**

AHEAD OF WHAT'S POSSIBLE™

Xilinx Zynq®-7000 All Programmable SoC ZC706 Evaluation Kit

EVAL-TPG-ZYNQ3

Features

- Upgraded with -3 speed grade XC7Z045 FFG900 supporting up to 12.5 Gbps JESD204B Lane Rates
- 1 GB (PS) DDR3 Component Memory
- 1 GB (PL) SODIM Memory
- GigE Ethernet
- USB Host
- FMC LPC and HPC Expansion Ports
- 12 V Adapter powers Zynq® Evaluation Kit
    - o   Provides Power for RadioVerse Transceiver Evaluation Boards via FMC Connector

Markets & Technology

Communications

Aerospace and Defense

- Electronic Surveillance and Countermeasures
- Milcom
- Unmanned Systems

RadioVerse: Concept to Creation at Lightspeed

Applicable Parts

- ADG707
- ADP123
- ADV7511

Package Contents

- ZC706 evaluation board upgraded with XC7Z045 FFG900-SOC
- Power supply
- For the user guide, please refer to the section "AD9371 Demonstration System Overview" of the AD9371 User Guide UG-992

Product Details

The Zynq®-7000 All Programmable SoC ZC706 Evaluation Kit Optimized for JESD204B provides a data capture platform for many of the RadioVerse families of wideband transceiver evaluation boards. A single 12 V power supply provides power for both the Zynq® and the RadioVerse evaluation boards. The data capture platform interfaces to the RadioVerse evaluation boards through a variety of interfacing standards including JESD207 and JESD204B. The JESD204B interface on the Zynq® evaluation system supports up to 12.5 Gbps lane rates. A variety of interface options allows the evaluation kit to interface directly to a PC monitor, keyboard, and mouse as well to a PC running the Transceiver Evaluation or Prototyping Software Packages.