#### 1 Scope

The present specifications shall apply to Sanken silicon diode, EM01A.

## 2 Outline

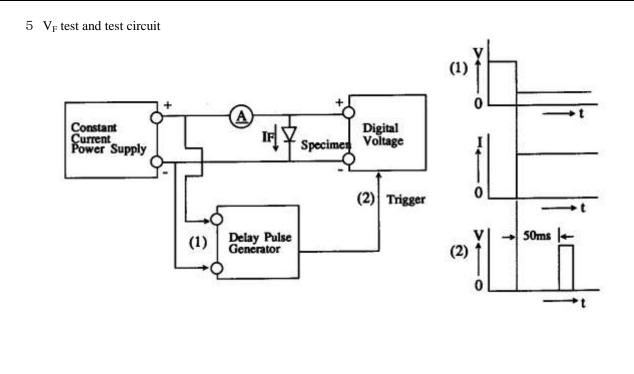
Туре	Silicon Rectifier Diode (Mesa type)				
Structure	Resin Molded Flammability: UL94V-0 (Equivalent)				
Applications	Commercial Frequency Rectification, etc				

## 3 Absolute maximum ratings

No.	Item	Symbol	Unit	Rating	Conditions
1 Transient Peak Reverse Voltage		V <sub>RSM</sub>	V	650	
2	Peak Reverse Voltage	V <sub>RM</sub>	V	600	
3	Average Forward Current	$I_{F(AV)}$	А	1.0	Refer to Derating curve in Section 6
4	Peak Surge Forward Current	I <sub>FSM</sub>	А	45	10ms. Half sine wave, one shot
5	Junction Temperature	$T_j$	°C	-40 to +150	
6	Storage Temperature	T <sub>stg</sub>	°C	-40 to +150	

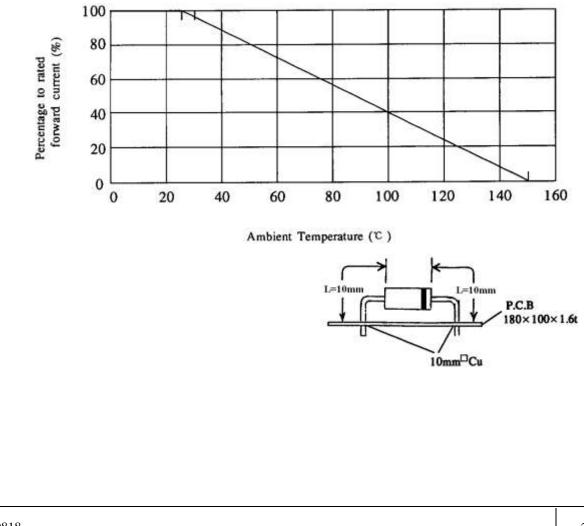
# 4 Electrical characteristics (Ta=25°C, unless otherwise specified)

No.	Item	Symbo 1	Unit	Rating	Conditions
1	Forward Voltage Drop	$V_{\rm F}$	V	0.97 max.	I <sub>F</sub> =1.0A
2	Reverse Leakage Current	I <sub>R</sub>	μΑ	10 max.	V <sub>R</sub> =V <sub>RM</sub>
3	Reverse Leakage Current Under High Temperature	H·I <sub>R</sub>	μΑ	50 max.	V <sub>R</sub> =V <sub>RM</sub> , Ta=100°C
4	Thermal Resistance	R <sub>th(j-l)</sub>	°C/W	20 max.	Between Junction and Lead

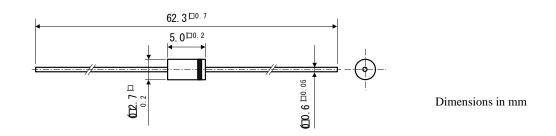


### 6 Derating

Derating to the ambient temperature. Power loss generated by voltage is not taken into consideration.



- 7 Package information
  - 7-1 Package type, physical dimensions and material

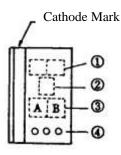


\*1 The allowance position of Body against the center of whole lead wire is 0.5mm(max.)
\*2 The centric allowance of lead wire against center of physical body is 0.2mm(max.)
\*3 The burr may exit up to 2mm from the body of lead

## 7-2 Appearance

The body shall be clean and shall not bear any stain, rust or flaw.

7-3 Marking



- ① Type number M0 as abbreviated of EM01A
- ② Class number: A
- ③ Lot number
  - A: Last digit of calendar year
  - B : Month (From 1 to 9 for Jan. to Sep. O for Oct. N for Nov. D for Dec.)

(4) Ten days (• first ten days, • • second ten days, ... third ten days)

The type No.  $V_{RM}$  rank and Lot No. are to be marked in white.