

RoHS

COMPLIANT HALOGEN

FREE Available

Vishay Siliconix

P-Channel 20-V (D-S) MOSFET

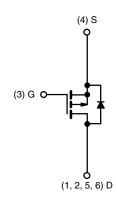
PRODUCT SUMMARY					
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)	Q _g (Typ.)		
	0.027 at V_{GS} = - 4.5 V	- 7			
- 20	0.035 at V_{GS} = - 2.5 V	- 6.2	21		
	0.048 at V _{GS} = - 1.8 V	- 5.2			

FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET: 1.8 V Rated
- Ultra-Low On-Resistance
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- Load Switch
- PA Switch
- Battery Switch



P-Channel MOSFET

		TSOP Top Vie		
Ť		1	6	
 3 mm 		2	5	
		3	4	
	◄	– 2.85 m	m —	

Ordering Information:	Si3493DV-T1-E3 (Lead (Pb)-free) Si3493DV-T1-GE3 (Lead (Pb)-free and Halogen-free)
Marking Code:	93xxx

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unles	ss otherwise r	noted		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 20		V
Gate-Source Voltage		V _{GS}	± 8		
	T _A = 25 °C	- I _D	- 7	- 5.3	
Continuous Drain Current $(T_J = 150 \ ^{\circ}C)^a$	T _A = 85 °C		- 3.6	- 3.9	
Pulsed Drain Current		I _{DM}	- 20		A
Continuous Source Current (Diode Conduction) ^a		۱ _s	- 1.7	- 0.9	
	T _A = 25 °C	- P _D	2.0	1.1	W
Maximum Power Dissipation ^a	T _A = 85 °C		1.0	0.6	vv
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manimum lumation to Analytica Id	t ≤ 5 s	R _{thJA} R _{thJF}	45	62.5	
Maximum Junction-to-Ambient ^a	Steady State		90	110	°C/W
Maximum Junction-to-Foot (Drain)	Steady State		25	30	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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Parameter	Symbol	Test Conditions Min.		Тур.	Max.	Unit	
Static				•			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 0.40		- 1	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, \text{ V}_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = -20 V, V_{GS} = 0 V$	-		- 1		
	IDSS	V_{DS} = - 20 V, V_{GS} = 0 V, T_{J} = 85 °C			- 5	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 V, V_{GS} = -4.5 V$	- 20			А	
	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 7 A		0.022	0.027		
Drain-Source On-State Resistance ^a		V_{GS} = - 2.5 V, I_D = - 6.2 A		0.029	0.035		
		V _{GS} = - 1.8 V, I _D = - 3 A		0.039	0.048		
Forward Transconductance ^a	g _{fs}	$V_{DS} = -5 V, I_{D} = -7 A$		25		S	
Diode Forward Voltage ^a	V _{SD}	$I_{\rm S}$ = - 1.7 A, $V_{\rm GS}$ = 0 V		- 0.7	- 1.2	V	
Dynamic ^b							
Total Gate Charge	Qg			21	32		
Gate-Source Charge	Q _{gs}	V_{DS} = - 10 V, V_{GS} = - 4.5 V, I_D = - 7 A		2.6		nC	
Gate-Drain Charge	Q _{gd}			6			
Turn-On Delay Time	t _{d(on)}			20	30		
Rise Time	t _r	V_{DD} = - 10 V, R_L = 10 Ω		40	60		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_{\text{D}}\cong$ - 1 A, V_{GEN} = - 4.5 V, R_{g} = 6 Ω		125	190	ns	
Fall Time	t _f			85	130		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.7 A, dl/dt = 100 A/μs		64	90		

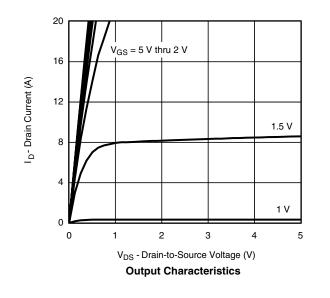
Notes:

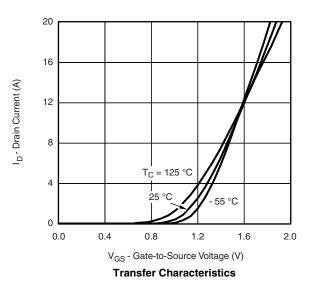
a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

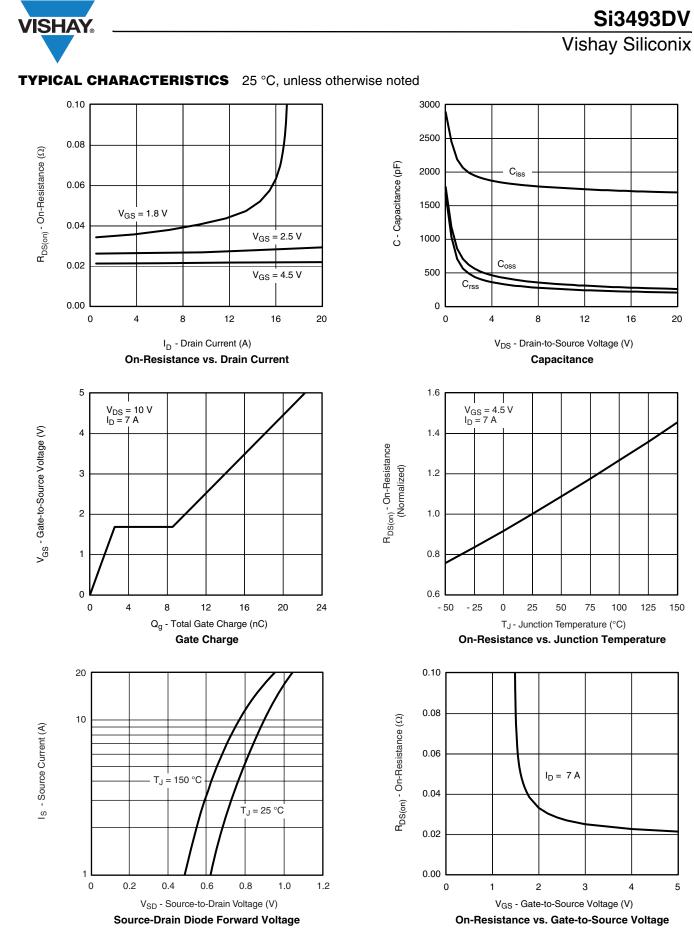
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







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20

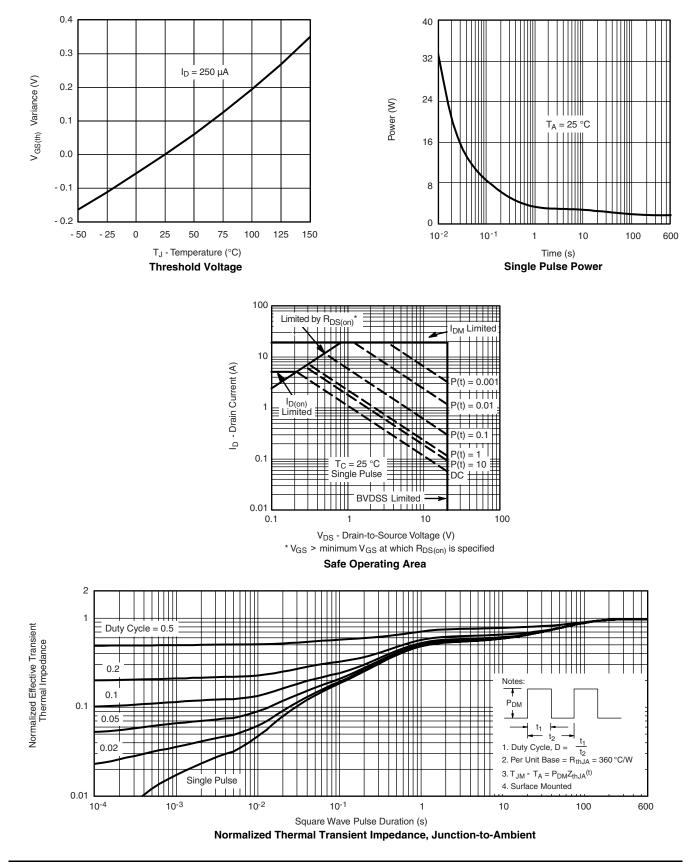
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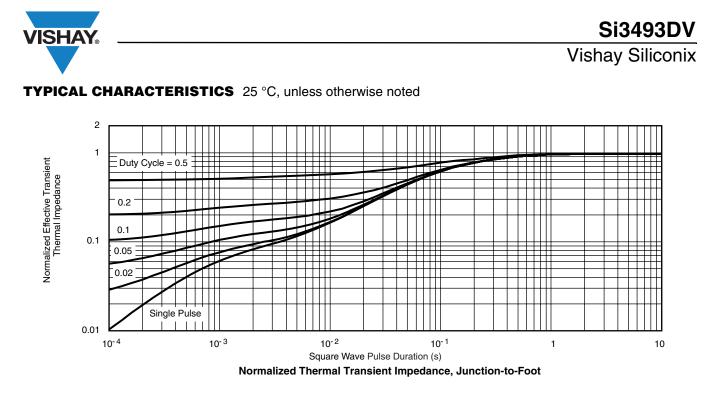
Si3493DV

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?71936.



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