### Vishay Semiconductors



Schottky Rectifier, 5.5 A





PRODUCT SUMMARY						
Package	D-PAK (TO-252AA)					
I <sub>F(AV)</sub>	5.5 A					
V <sub>R</sub>	100 V					
V <sub>F</sub> at I <sub>F</sub>	See Electrical table					
I <sub>RM</sub>	4 mA at 125 °C					
T <sub>J</sub> max.	150 °C					
Diode variation	Single die					
E <sub>AS</sub>	6 mJ					

### FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- Popular D-PAK outline
- Small foot print, surface mountable
- High frequency operation
- AEC-Q101 qualified
- Meets JESD 201 class 2 whisker test
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### DESCRIPTION

The VS-50WQ10FNHM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	DL CHARACTERISTICS VALUES					
I <sub>F(AV)</sub>	Rectangular waveform	5.5	А			
V <sub>RRM</sub>		100	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	330	А			
V <sub>F</sub>	5 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.63	V			
TJ	Range	- 40 to 150	۵°			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-50WQ10FNHM3	UNITS		
Maximum DC reverse voltage	V <sub>R</sub>	100	V		
Maximum working peak reverse voltage	V <sub>RWM</sub>	100	v		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS	
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at $T_{C}$ = 135 °C, rectangular waveform		5.5		
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	330	А	
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	110		
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 0.5 A, L = 40 mH		6.0	mJ	
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		0.5	A	

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# VS-50WQ10FNHM3

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ELECTRICAL	SPECIFICATIONS
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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS VALUES U			UNITS
		5 A	T.I = 25 °C	0.77	V
Maximum forward voltage drop	V <sub>EM</sub> <sup>(1)</sup>	10 A	1j=25 0	0.91	
See fig. 1	VFM (*)	5 A	T. = 125 °C	0.63	
		10 A	1j = 125 C	0.74	
Maximum reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C		1	mA
See fig. 2	IRM ( )	T <sub>J</sub> = 125 °C	$V_R = Rated V_R$	4	
Threshold voltage	V <sub>F(TO)</sub>	$T_{\rm J} = T_{\rm J} \text{ maximum} \qquad \qquad$		0.47	V
Forward slope resistance	r <sub>t</sub>			mΩ	
Typical junction capacitance	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C 183 pF		pF	
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body 5.0 nH			nH

#### Note

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 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		- 40 to 150	°C	
Maximum thermal resistance, junction to case	R <sub>thJC</sub>	DC operation See fig. 4	3.0	°C/W	
Approximate weight			0.3	g	
Approximate weight			0.01	oz.	
Marking device		Case style D-PAK	50WQ	10FNH	

### Note

(1)

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink

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Fig. 1 - Maximum Forward Voltage Drop Characteristics







Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



Fig. 4 - Maximum Thermal Impedance  $\mathsf{Z}_{\mathsf{thJC}}$  Characteristics

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1000

10 000

100

Fig. 7 - Maximum Non-Repetitive Surge Current

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;  $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 

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**ORDERING INFORMATION TABLE** 

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Device code	VS-	50	w	Q	10	FN	TRL	н	М3
		(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)
	$\bigcirc$	$\bigcirc$	Ċ	$\bigcirc$	C	Ċ	$\bigcirc$	Ċ	C
	1	- Visl	nay Sen	niconduo	ctors pro	oduct			
	2	- Cur	rent rati	ng (5.5 /	A)				
	3	- Pac	kage id	entifier:					
		W =	D-PAK						
	4	- Sch	ottky "C	)" series					
	5	- Volt	tage rati	ng (10 =	= 100 V)				
		- FN	= TO-2	52AA (D	-PAK)				
	7	• N	one = T	ube					
		• TI	R = Tap	e and re	el				
		• TI	RL = Ta	pe and r	eel (left	oriente	d)		
		• TF	R = Ta	pe and	reel (rig	ht orien	ted)		
	8	- H=	AEC-Q	101 qua	alified				
	9	- Env	vironme	ntal digit					
		M3	= Halog	jen-free,	RoHS-	complia	nt, and	termina	itions lea

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-50WQ10FNHM3	75	3000	Antistatic plastic tube			
VS-50WQ10FNTRHM3	2000	2000	13" diameter reel			
VS-50WQ10FNTRRHM3	3000	3000	13" diameter reel			
VS-50WQ10FNTRLHM3	3000	3000	13" diameter reel			

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95519				
Part marking information	www.vishay.com/doc?95518				
Packaging information	www.vishay.com/doc?95033				

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