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REFERENCE SCHEMATIC 5817

Novato (MAXREFDES16#): 4-20mA Loop-Powered Temperature Sensor with HART

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Abstract: The Novato reference design (MAXREFDES16#) is a 16-bit, high-accuracy, loop-powered temperature transducer that transmits temperature information from a remote object to the central control unit over a 4–20mA current loop and using the highway addressable remote transducer (HART) communication protocol.

Introduction

Temperature is one of the most widely measured parameters in industrial process control and automation. This reference design provides a complete signal-chain solution that works with any type of RTDs, from PT100 to PT1000. The Novato PT100 2-wire, loop-powered smart temperature transmitter guarantees a low-power, easy-to-use, reliable solution of temperature measurement from -200°C to +850°C with accuracy better than 0.1% or 1.0°C, whichever is more accurate, over the entire operating range.

The Novato MAXREFDES16 smart sensor transmitter reference design features:

- 2-, 3-, or 4-wire RTD sensor input
- -200°C to +850°C measured temperature range
- -40°C to +85°C operating range
- 0.03°C temperature resolution
- < 0.05% span error or ±0.2°C at +23°C (whichever is greater)
- < 0.1% span error or ±1.0°C over operating range
- 12V to 40V supply voltage range
- 200µA RTD excitation current
- Auto compensation of wire resistance for 3-wire RTD
- 4-20mA current loop output
- 10½ to 1k½ loop load Highway addressable remote transducer (HART) communication protocol requires minimum load of 230½
- Sensor burnout detection NAMUR down scale: < 3.2mA NAMUR up scale: programmable from 20.5mA to > 24mA
- Loop current limiter: 30mA ±5mA
- HART protocol compatible

Detailed Description

Block diagram of the Novato smart sensor transmitter is shown in Figure 1.



More detailed image.

Figure 1. Novato (MAXREFDES16#) block diagram.

The signal chain contains the MAX11213 (U1), 16-bit delta-sigma ADC with programmable gain and GPIO, the RL78/G13 Renesas' microcontroller with 128kB flash memory (U6), and the 4–20mA current loop transmitter built on the MAX5216 and MAX9620 (U2 and U3). The DS8500 modem (U7) provides digital interface utilizing HART protocol over 4–20mA current loop. The MAX15006 (U5) and MAX6133 (U4) provide power and reference voltage to the entire circuitry.

For more detailed information about 4–20mA current loop transmitter refer to reference schematic 5610, "High-Performance, High-Accuracy 4-20mA Current-Loop Transmitter Meets Toughest Industrial Requirements."

This design uses a ratiometric measurement of sensor resistance according to the ADC transfer function (Eq. 1 and Eq. 2):

 $ADCcode = ((V_{AINP} - V_{AINN}))/(V_{REF}/gain) \times (2^{16} - 1)$ (Eq. 1)

where

 $(V_{AINP} - V_{AINN}) = I_{RTD} \times R_{RTD}$ is a voltage drop on RTD,

 $V_{REF} = I_{RTD} \times R_{REF}$ is a voltage drop on reference resistor R_{REF}

Equation 1 can be rewritten as:

 $ADCcode = R_{RTD}/(R_{REF}/gain) \times (2^{16} - 1)$ (Eq. 2)

Then the ADC code is converted into temperature data according to the Callendar-Van Dusen equation (Eq. 3):

$$R(T) = R_0(1 + A \times T + B \times T^2 - 100 \times C \times T^3 + C \times T^4)$$
(Eq. 3)

where

R(T) is RTD resistance at temperature T (°C)

 R_0 is RTD resistance at $0^\circ C$

A, B, and C constants are derived from experimentally determined parameters and regulated by the IEC751 standard; they also must be provided by RTD manufacturers

For PT100 and temperature coefficient of resistance: $\alpha = 0.003850 [\alpha = (R_{100} - R_0)/(100 \times R_0)]$

 $A = 3.90830 \times 10^{-3}$

 $B = -5.77500 \times 10^{-7}$

C = 4.18301×10^{-12} for -200° C ² T ² 0° C

C = 0 for 0°C 2 T 2 850°C.

Temperature T can be calculated by solving quadratic equation if we ignore coefficient C = 4.18301×10^{-12} for negative temperature (Eq. 4):

$$T(^{\circ}C) = (-A + \tilde{A}(A^2 - 4B(1 - R_{RTD}/R_0)))/2B$$

where

 $R_{RTD} = (ADCcode \times R_{REF})/((2^{16} - 1) \times gain)$

After that, temperature is converted into 4–20mA loop current using equation Eq. 5:

Thus, loop current is proportional to the temperature (T).

The MAX11213 ADC allows auto compensation of the sensor wire resistance by measuring that resistance during the calibration procedure and continuously subtracting that value from each conversion result. The wire resistance compensation technique is a one-step process called system offset calibration.

In normal operation the ADC measures the voltage drop between points 2 and 3 of connector J1, see **Figure 2**. For the offset calibration, the **MAX4729** switches into the NO (normally opened) position and the ADC measures the voltage between points 1 and 2 of connector J1. In other words, the ADC measures wire resistance and automatically stores that value into the System Offset Calibration register.



Figure 2. Sensor's wire measurement using the MAX4729 mux.

The central control unit (CCU) or programmable logic controller (PLC) can convert the loop current back to the temperature using Eq. 6:

(Eq. 6)

(Eq. 4)

where

Tmax is the maximum measured temperature in °C

Tmin is the minimum measured temperature in °C

 $\mathsf{I}_{\mathsf{LOOP}}$ is the measured loop current in mA.

The Novato (MAXREFDES16#) smart sensor transmitter board is loaded with the firmware that supports any RTD, from PT100 to PT1000. Selection of the right sensor can be done during calibration.

Novato Calibration Procedure and Software

Calibration of the sensor is easy with the supporting Novato calibration software and USB-to-UART programming adapter, Figure 3.



Figure 3. Novato USB-to-UART programming adapter.



Figure 4. Novato smart sensor transmitter.

The programming adapter is connected to a PC through the USB port, and to the Novato board J4 header with a 2-wire UART interface, Figure 4.

Connect the Novato board to the 4–20mA current loop and supply the power according to the block diagram in Figure 1.

Plug in the USB-to-UART programming adapter to the PC and start the Novato calibration software, **Figure 5**. Wait until the PC enumerates the USB device and the software reports that the USB adapter is connected.

Novato Calibration Software	
Eile Help	Calibration Mode (disables main loc
Calibration Debug ADC	
ADC (MAX11213)	
1. System Offset 2. System Gain Description:	Calibrate Figure 1. Setup
For the System Offset calibration, short all fou on J1 as shown in Figure 1. Then click the Cal button.	Ir inputs librate
DAC (MAX5216)	RAM Calibration Values
4mA DAC Value	ADC System Offset FFFFADh
20mA 2690 + Hex	ADC System Gain 7FFFAEh
Description:	ADC Self Offset FFFFFh
To find the 4mA DAC value, enter 2690 for	ADC Self Gain BFD554h
100 Ω load in the DAC Value edit box and	4mA DAC Value 2690
the current measured through the loop load	20mA DAC Value 50901
resistor is as close as possible to 4mA.	Read
	Save To Flash Reset to Flash
itatus Log	Clear Log
JSB Adaptor not Detected! JSB Adaptor not Detected! Calibration Mode Enabled ADC Command Sent All Registers Read	
mware Version: 1.0 Software Version 1	1.0 USB Adaptor Connected

Figure 5. Novato calibration GUI.

The GUI is user friendly and easy to navigate. Just follow the tips in the Status box.

Step 1: Disable main loop to allow communication to the sensor by selecting the Calibration Mode checkbox. Note all functions are disabled (grayed) until Calibration mode is enabled.

Step 2: Select RTD.

In the Calibration tab, clicking the "RTD Settings" button opens the RTD Settings window, **Figure 6**. Then select the appropriate R_0 resistance at 0°C of RTD, which is going to be connected to the Novato board, e.g. 100½, gain, max and min temperature.

ADC Gain	16	•	
Ro	100	+-	Ω
Maximum Temperature	850	+	°C
Minimum Temperature	-200	+ -	°C
Read	Write		1

Figure 6. RTD Settings pop up window.

Use the appropriate settings from Table 1 and click the Write button. Click the Read button to verify the correct parameters are written.

Table 1. RTD Parameters				
Parameter	PT100	PT200	PT500	PT1000
R ₀ ()	100	200	500	1000
Gain	16	16	8	4
Max. Temperature (°C)	850	630	630	630
Min. Temperature (°C)	-200	-200	-200	-200

Step 3: Perform system offset calibration.

Short J1.1, J1.2, J1.3, and J1.4 of the sensor input connector. Select the System Offset radio button and click the Calibrate button.

Tip: Press the Read button to check how the values are changed during calibration.

Step 4: Perform system gain calibration.

Short J1.1, J1.2, and J1.4; connect J1.3 to the VGND test point. Select the System Gain radio button and click the Calibrate button.

Step 5: Perform 20mA loop current calibration.

Select the 20mA radio button, enter a value in the DAC Value edit box and click the Calibrate button. This value must be found manually by selecting a DAC code and measuring the loop current by external digital multimeter (DMM). Adjust the DAC code until the loop current measures 20mA.

Step 6: Perform 4mA loop current calibration.

Select the 4mA radio button, enter a value in the DAC Value edit box and click the Calibrate button. This value must be found manually by selecting a DAC code and measuring the loop current by external DMM. Adjust the DAC code until the loop current measures 4mA.

Step 7 (optional): Adjust ADC Gain and Offset for specific RTD.

Use a Fluke 724 process calibrator to simulate the RTD. For example, select PT100 RTD as a source and connect Fluke 724 to connector J1 as a 2-wire RTD, Figure 7.



Figure 7. 2-wire RTD connection.

In the Debug ADC tab, select the "Convert 5sps" ADC Command from the pulldown menu, Figure 8.

6.1. Click the "Send" button.

6.2. Click the "Read" button in the ADC Registers group box.

The ADC Registers are read back, Figure 8.

6.3. Click the "0%" button on Fluke 724 to select -200°C.

6.4. Repeat 6.1. and 6.2. The ADC Data register should read 0x50F or something close to that value.

6.5. Click the "100%" button on Fluke 724 to select +800°C.

6.6. Repeat 6.1 and 6.2. The ADC Data register should read 0x6697 or something close to that value. Manually adjust Self Gain and Self Offset registers until the following ADC Data is read (**Table 2**).

Table 2. Calibration Points for PT100				
Fluke 724 RTD Source	ADC Data (hex)			
-200.0°C	50F			
0.0°C	1B4E			
800.0°C	6697			

Tip: For -200°C, start adjusting the Self Offset register until it reaches FFFFFh value and then continue adjusting the SYS Offset register.

After that, set up 800°C and adjust the Self Gain register starting from the 3rd LSB digit until 6697 is read.

Eile	Help			Calibration Mo	de (dis	sables main lo
Calib	oration Debug	ADC				
	ADC Registers	(Hex)		ADC Commands		
	Data	1B44	+	Convert 5sps	•	Send
	Status	21	+			
	CTRL1	5E	+	ADC Read/Write (Hex)		
	CTRL2	1E	+	Num Bytes	1	<u>+</u>
	CTRL3	80	+	Command Byte	0	+
	SYS Offset	FFFFAD	+	Data Byte 1 (MSB)	0	+
	SYS Gain	7FFFAE	+	Data Byte 2	0	*
	Self Offset	FFFFFF	+	Data Byte 3	0	+
	Self Gain	BFD554	+			
	Re	ad		Read	Wr	ite
			_			
Statu	s Log					Clear Lo
JSB USB Calibr ADC All Re	Adaptor not Detect Adaptor not Detect ration Mode Enab Command Sent egisters Read	tted! tted! led				

Figure 8. Debug ADC tab.

Step 8: Save to Flash.

Click the "Save To Flash" button in the Calibration tab to store calibration values into nonvolatile memory.

Step 9: Disable Calibration Mode.

Resume normal operation by deselecting the Calibration Mode checkbox.

All Design Files

Download All Design Files

Hardware Files Schematic Bill of materials (BOM) PCB Gerber Novato firmware Novato calibration software Note, the Novato calibration software only works with custom made USB-to-UART programming adapter. This adapter is not provided with Novato board and has to be requested separately.

Firmware Files

Firmware is developed using Renesas' e2 studio—complete development and debug environment based on the popular Eclipse CDT project. Use Renesas' E1 Emulator to download firmware through connector J3. The E1 Emulator also provides all the basic debugging function. For more information regarding RL78 MCU, e2 studio IDE, and E1 emulator, refer to Renesas' website.

The Novato reference design comes with firmware loaded on the system. For additional details, please contact the factory.

HART Communication Protocol

The Novato reference design fully satisfies the HART physical layer requirements. The HART functions set is developed by AB Tech Solution, an engineering firm specialized in product development services for industrial automation applications.

The HART stack for the Novato reference design supports all universal and common practice commands. The HART stack is not provided by Maxim Integrated. Contact **AB Tech Solution** regarding licensing, integration, and further development of device-specific functions.

Lab Measurements



All measurements are taken with the Fluke 724 process calibrator for PT100 RTD. The 4–20mA loop current is measured by the Agilent 34401A digital multimeter as voltage drop over 100½ load resistor. Measurement results are shown in **Figure 9** and **Figure 10**.

Figure 9. Error (%) change vs. temperature.



Figure 10. Error (°C) change vs. temperature.

The Novato design fits into standard head mounting DIN form B (< 40mm diameter) enclosure.

Buy Reference Design

Buy Direct: Novato (MAXREFDES16#)

Or

Order the Novato reference design (MAXREFDES16#) from your local Maxim representative.

Related Parts		
DS8500	HART Modem	Free Samples
MAX11213	16-Bit, Single-Channel, Ultra-Low Power, Delta-Sigma ADC with Programmable Gain and GPIO	Free Samples
MAX15006	40V, Ultra-Low Quiescent-Current Linear Regulators in 6-Pin TDFN/8-Pin SO	Free Samples
MAX4729	Low-Voltage 3.51/2, SPDT, CMOS Analog Switches	Free Samples
MAX5216	14-/16-Bit, Low-Power, Buffered Output, Rail-to-Rail DACs with SPI Interface	Free Samples
MAX6133	3ppm/°C, Low-Power, Low-Dropout Voltage Reference	Free Samples
MAX9620	High-Efficiency, 1.5MHz Op Amps with RRIO	Free Samples

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