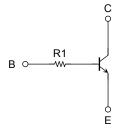
TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT process) (Bias Resistor built-in Transistor)

## **RN1673**

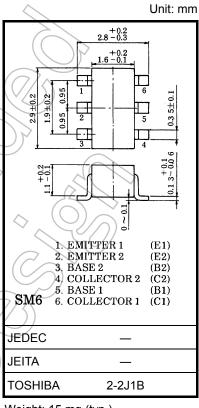
Switching Applications
Inverter Circuit Applications
Interface Circuit Applications
Driver Circuit Applications

- Two devices are incorporated into a Super-Mini (6 pin) package
- Incorporating a bias resistor into a transistor reduces parts count. Reducing
  the parts count enable the manufacture of ever more compact equipment
  and save assembly cost.

### **Equivalent Circuit and Bias Resistor Values**



R1: 47 kΩ (Q1, Q2 common)

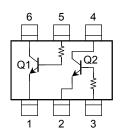


Weight: 15 mg (typ.)

### Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	50	V
Collector-emitter voltage	V <sub>CEO</sub>	50	V
Emitter-base voltage	V <sub>EBO</sub>	5	V
Collector current	IĈ)	100	mA
Collector power dissipation	P <sub>C</sub> (Note*)	300	mW
Junction temperature	Ti	150	°C
Storage temperature range	T <sub>stg</sub>	-55 to 150	°C

# Equivalent Circuit (top view)



Note\*: Total rating

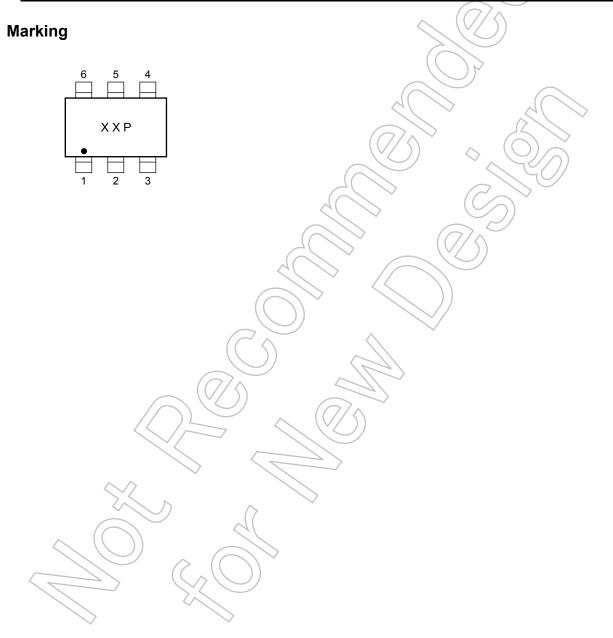
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e.operatingtemperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production 2003-12

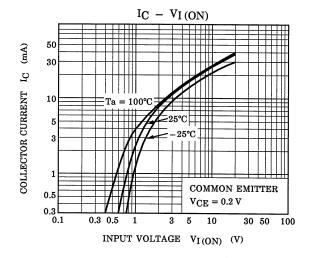
### Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

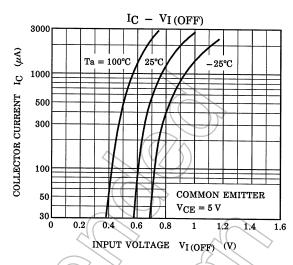
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	$V_{CB} = 50 \text{ V}, I_{E} = 0$	_	_	100	nA
Emitter cut-off current	I <sub>EBO</sub>	V <sub>EB</sub> = 5 V, I <sub>C</sub> = 0	_	_	100	nA
DC current gain	h <sub>FE</sub>	$V_{CE} = 5 \text{ V, } I_{C} = 1 \text{ mA}$	120	_	700	
Collector-emitter saturation voltage	V <sub>CE</sub> (sat)	$I_C = 5 \text{ mA}, I_B = 0.25 \text{ mA}$		0.1	0.3	V
Input resistor	R1	_	32.9	) 47	61.1	kΩ

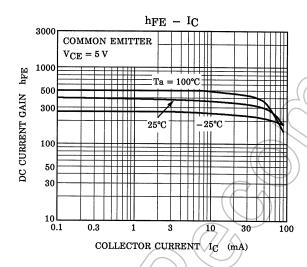


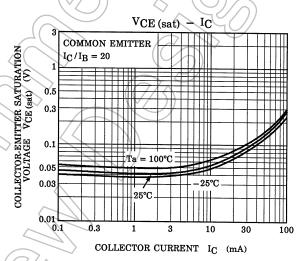
2 2014-03-01

### (Q1, Q2 common)









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