CXT4033

SURFACE MOUNT PNP SILICON TRANSISTOR



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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CXT4033 type is an PNP silicon transistor manufactured by the epitaxial planar process, epoxy molded in a surface mount package, designed for high current general purpose amplifier applications.

MARKING: FULL PART NUMBER



MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Collector-Base Voltage	V_{CBO}	80	V
Collector-Emitter Voltage	V_{CEO}	80	V
Emitter-Base Voltage	V_{EBO}	5.0	V
Continuous Collector Current	IC	1.0	Α
Peak Collector Current	I _{CM}	1.5	Α
Power Dissipation	P_{D}	1.2	W
Operating and Storage Junction Temperature	T _{J,} T _{stg}	-65 to +150	°C
Thermal Resistance	$\Theta_{\sf JA}$	104	°C/W

ELECTRICAL CHARACTERISTICS: (T_A=25°C unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
I _{CBO}	V _{CB} =60V		50	nA
I _{EBO}	V _{EB} =5.0V		10	nA
BV _{CBO}	I _C =10μA	80		V
BV _{CEO}	I _C =10mA	80		V
BVEBO	I _E =10μA	5.0		V
V _{CE} (SAT)	I _C =150mA, I _B =15mA		0.15	V
V _{CE(SAT)}	I _C =500mA, I _B =50mA		0.50	V
V _{BE} (SAT)	I _C =150mA, I _B =15mA		0.90	V
V _{BE(SAT)}	I _C =500mA, I _B =50mA		1.10	V
h _{FE}	V_{CE} =5.0V, I_{C} =0.1mA	75		
hFE	V_{CE} =5.0V, I_{C} =100mA	100	300	
hFE	V_{CE} =5.0V, I_{C} =500mA	70		
h _{FE}	V_{CE} =5.0V, I_{C} =1.0A	25		
f _T	V_{CE} =10V, I_{C} =50mA, f=1.0MHz	100		MHz
C _{ob}	V_{CB} =10V, I_E =0, f=1.0MHz		20	pF
C _{ib}	V_{EB} =0.5V, I_{C} =0, f=1.0MHz		110	pF

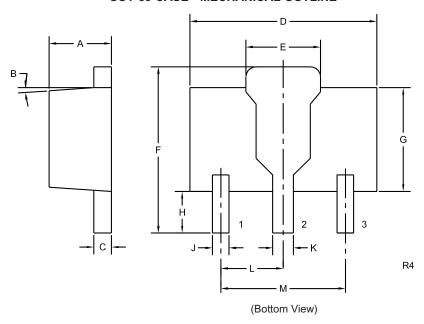
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SOT-89 CASE - MECHANICAL OUTLINE



LEAD CODE:

- Emitter
 Collector
- 3) Base

MARKING:

FULL PART NUMBER

DIMENSIONS							
	INCHES		MILLIMETERS				
SYMBOL	MIN	MAX	MIN	MAX			
Α	0.055	0.067	1.40	1.70			
В	4°		4°				
С	0.014	0.018	0.35	0.46			
D	0.173	0.185	4.40	4.70			
Е	0.064	0.074	1.62	1.87			
F	0.146	0.177	3.70	4.50			
G	0.090	0.106	2.29	2.70			
Н	0.028	0.051	0.70	1.30			
J	0.014	0.019	0.36	0.48			
K	0.017	0.023	0.44	0.58			
Ĺ	0.059		1.50				
М	0.118		3.00				

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OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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